MULTIMEDIA UNIVERSITY

STUDENT ID NO

MULTIMEDIA UNIVERSITY

FINAL EXAMINATION

TRIMESTER 1, 2019/2020

EEN1016 – ELECTRONICS I

(TE, RE)

16 OCTOBER 2019
9:00 a.m – 11:00 a.m
(2 Hours)

INSTRUCTIONS TO STUDENT

1. This Question paper consists of **5** pages excluding cover page with **4** Questions only.

2. Attempt **ALL** questions in the Question Paper. All questions carry equal marks and the distribution of the marks for each question is given.

3. Please write all your answers in the Answer Booklet provided.

QUESTION 1

a) Determine the electron configurations of sodium (Na, atomic number) and copper (Cu, atomic number). What is the common factor between the two materials?

[7 marks]

b) Calculate the majority and minority carrier concentrations in silicon with $n_i = 1.1\times10^{10}\,cm^{-3}$ at T = 300K if doping densities are given as

(i) $N_A = 10^{16}\,cm^{-3},\ N_D = 0$

[4 marks]

(ii) $N_D = 3\times10^{15}\,cm^{-3},\ N_A = 0$

[4 marks]

c) Illustrate on how a DC battery is connected to a PN junction to make it forward bias and reverse bias by indicating the polarities of the battery. Explain the width difference of depletion region during forward bias and reverse bias modes.

[10 marks]

Continued .....

QUESTION 2

a) A $6.8V$ Zener diode is used for voltage regulation in the circuit shown in Figure Q2(a). The supply voltage, V_s can vary between $10V$ to $15V$. The minimum load current is $0mA$. The minimum Zener diode current is $1.5mA$. The power dissipation of the Zener diode must not exceed $700mW$ at room temperature. Determine the followings:

(i) Maximum permissible value of Zener diode current I_{ZM}.

[2 marks]

(ii) Value of R_s that limits the Zener diode current to the value determined in part (i).

[3 marks]

(iii) Maximum load current.

[3 marks]

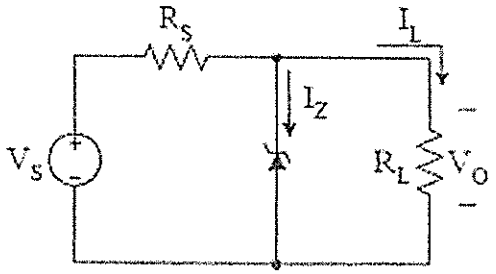

Figure Q2(a)

Continued .....

b) A half wave rectifier with a capacitor filter is shown in Figure Q2(b). V_s is 120V in root mean square (RMS) format at 50 Hz, $C = 1000\mu F$ and $R_L = 1k\Omega$. Voltage drop across conducting diode is 0.7V. Calculate the followings:

(i) Maximum output voltage $V_{0\max}$.

[4 marks]

(ii) Maximum output current $I_{0\max}$.

[2 marks]

(iii) Average output voltage V_{0dc}.

[2 marks]

(iv) Ripple voltage.

[2 marks]

(v) Ripple factor.

[2 marks]

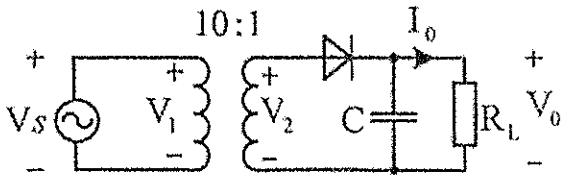

Figure Q2(b)

c) You are given a 230V AC supply. You are required to generate 10V DC with very low ripple. Identify the various components you need. Explain why they are needed and interconnect them appropriately.

[5 marks]

Continued .....

QUESTION 3

A bipolar junction transistor (BJT) circuit is shown in Figure Q3. Given that V_{BB} = 8V, R_B = 50kΩ, V_{CC} = 15V, R_C = 2kΩ, R_E = 2kΩ and the DC current gain ratio, β of the BJT is 100. Assume the forward biased base to emitter voltage, $V_{BE(ON)}$ = 0.7V.

(i) Calculate all node voltages (V_B, V_C, V_E) and branch currents (I_B, I_C, I_E) via DC analysis.

[14 marks]

(ii) In what region does this BJT circuit operate in? Justify your answer.

[5 marks]

(iii) Determine the maximum value of R_C for which the BJT will still operate in the region as mentioned in (ii).

[6 marks]

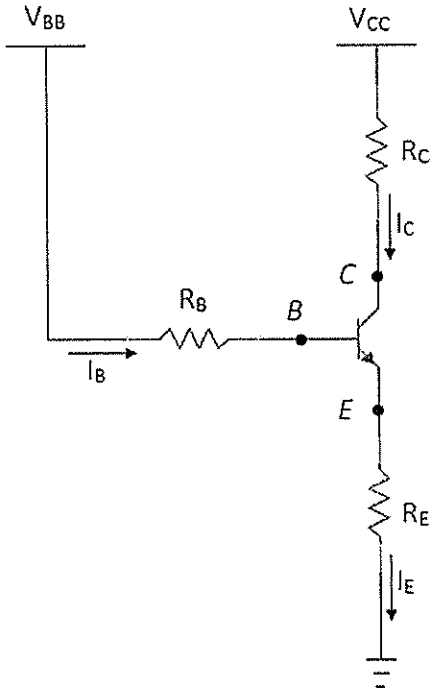

Figure Q3

Continued .....

QUESTION 4

Figure Q4 shows a common emitter configured BJT amplifier circuit. Given that $R_S = R_C = 10k\Omega$, $R_F = 200k\Omega$, $R_E = 5k\Omega$, $C_1 = C_2 = C_3 = 1\mu F$ and hybrid h parameters as follows:

$h_{ie} = 1.1k\Omega$ $h_{fe} = 60$ $h_{oe} = 0.1mS$ $h_{re} = 0$

(i) Draw the complete hybrid h small signal circuit by applying the Miller's theorem. Label all components in the circuit.

[8 marks]

(ii) Derive the expression of R_{out} and calculate its value.

[6 marks]

(iii) Derive the expression of R_{in} and calculate its value.

[11 marks]

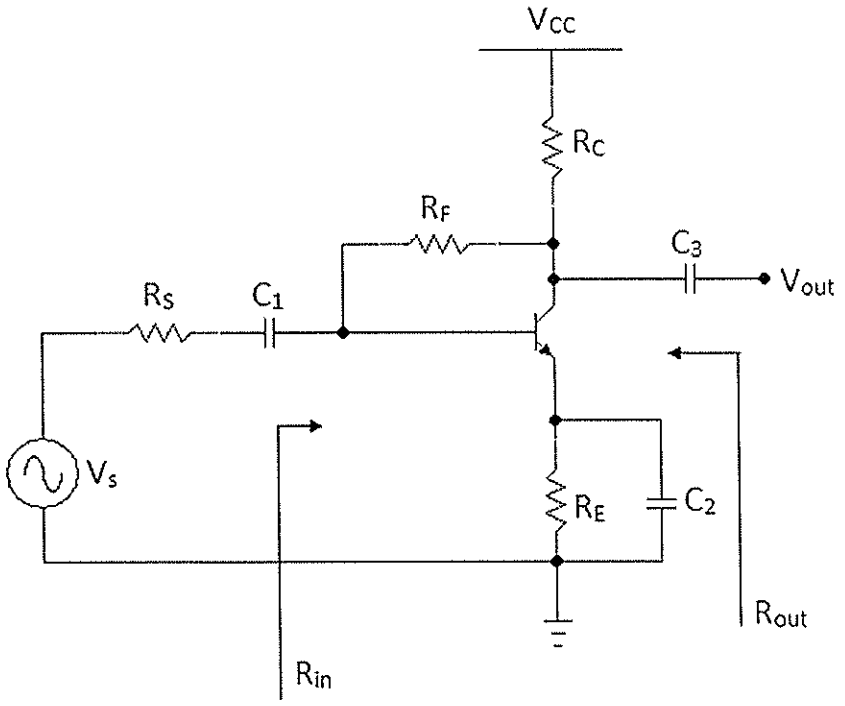

Figure Q4

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